

Table 7-20 (continued)
Expansion slot signals

Pin	Signal	Description
30	IRQ'	Interrupt request to 65C02. Pulling this line low starts an interrupt cycle only if the interrupt-disable (I) flag in the 65C02 is not set. Uses the interrupt-handling routine at location \$03FE. This line has a 3300 ohm pullup resistor to +5V.
31	RES'	Pulling this line low initiates a reset routine, as described in Chapter 4.
32	INH'	Pulling this line low during $\phi 1$ inhibits (disables) the memory on the main circuit board. This line has a 3300 ohm pullup resistor to +5V.
33	-12V	-12 volt power supply. A total of 200mA is available for all peripheral cards.
34	-5V	-5 volt power supply. A total of 200mA is available for all peripheral cards.
35	3.58M	3.58 MHz color reference signal, on slot 7 <i>only</i> . This line can drive 2 LS TTL loads.*
36	7M	System 7 MHz clock. This line can drive 2 LS TTL loads.*
37	Q3	System 2 MHz asymmetrical clock. This line can drive 2 LS TTL loads.*
38	$\phi 1$	65C02 phase 1 clock. This line can drive 2 LS TTL loads.*
39	μ PSYNC	The 65C02 signals an operand fetch by driving this line high during the first read cycle of each instruction.
40	$\phi 0$	65C02 phase 0 clock. This line can drive 2 LS TTL loads.*
41	DEVICE SELECT'	Normally high; goes low during $\phi 0$ when the 65C02 addresses location \$C0nX, where n is the connector number plus 8. This line can drive 10 LS TTL loads.*