

Table 7-19 (continued)
Game I/O connector signals

Internal connector pin	Back-panel connector pin	Signal	Description
8	3	GND	System ground.
15,14, 13,12	—	AN0–AN3	Annunciators. These are standard 74LS TTL outputs and must be buffered to drive other than TTL inputs.
9,16	—	n. c.	Nothing is connected to these pins.

Chapter 6 describes the standards for programming peripheral cards for the Apple IIe.

Expanding the Apple IIe

The main circuit board of the Apple IIe has eight empty card connectors or slots on it. These slots make it possible to add features to the Apple IIe by plugging in peripheral cards with additional hardware. This section describes the hardware that supports them, including all of the signals available on the expansion slots.

The expansion slots

The seven connectors lined up across the back part of the Apple IIe's main circuit card are the expansion slots, also called peripheral slots or simply slots, numbered from 1 to 7. They are 50-pin PC-card edge connectors with pins on 0.10-inch centers. A PC card plugged into one of these connectors has access to all of the signals necessary to perform input and output and to execute programs in RAM or ROM on the card. These signals are described briefly in Table 7-20. The following paragraphs describe the signals in general and mention a few points that are often overlooked. For further details, refer to the schematic diagram in Figures 7-15a–d (Figure 7-16a–d for the extended keyboard IIe).