

**Table 7-20** (continued)  
Expansion slot signals

| Pin | Signal      | Description  |
|-----|-------------|--|
| 19  | SYNC'       | Composite horizontal and vertical sync, on expansion slot 7 <i>only</i> . This line can drive 2 LS TTL loads.*   |
| 20  | I/O STROBE' | Normally high; goes low during ø0 when the 65C02 addresses a location between \$C800 and \$CFFF. This line can drive 4 LS TTL loads.   |
| 21  | RDY         | Input to the 65C02. Pulling this line low during ø1 halts the 65C02 with the address bus holding the address of the location currently being fetched. This line has a 3300 ohm pullup resistor to +5V. |
| 22  | DMA'        | Input to the address bus buffers. Pulling this line low during ø1 disconnects the 65C02 from the address bus. This line has a 3300 ohm pullup resistor to +5V.   |
| 23  | INT OUT     | Interrupt priority daisy-chain output. Usually connected to pin 28 (INT IN).†  |
| 24  | DMA OUT     | DMA priority daisy-chain output. Usually connected to pin 22 (DMA IN).   |
| 25  | +5V         | +5-volt power supply. A total of 500mA is available for all peripheral cards.  |
| 26  | GND         | System common ground.  |
| 27  | DMA IN      | DMA priority daisy-chain input. Usually connected to pin 24 (DMA OUT).   |
| 28  | INT IN      | Interrupt priority daisy-chain input. Usually connected to pin 23 (INT OUT).   |
| 29  | NMI'        | Nonmaskable interrupt to 65C02. Pulling this line low starts an interrupt cycle with the interrupt-handling routine at location \$03FB. This line has a 3300 ohm pullup resistor to +5V.               |