
SSC firmware memory use

Table H-8 is an overall map of the locations that the SSC uses, both in the Apple IIe and in the SSC's own firmware address space.

Table H-8
Memory use map

Address	Name of area	Contents
\$0000-\$00FF	Page zero	Monitor pointers, I/O hooks, and temporary storage.
\$04xx-\$07xx	Peripheral slot RAM	Locations (8 per slot) in Apple IIe pages \$04 through \$07. SSC uses all 8 of them.
\$C0(8+s)0-\$C0(8+s)F	Peripheral card I/O space	Locations (16 per slot) for general I/O. SSC uses 6 bytes.
\$Cs00-\$CsFF	Peripheral card ROM space	One 256-byte page reserved for card in slot s; first page of SSC firmware.
\$C800-\$CFFF	Expansion ROM	Eight 256-byte pages reserved for 2K ROM or PROM. SSC maps its firmware onto \$C800-\$CEFF.